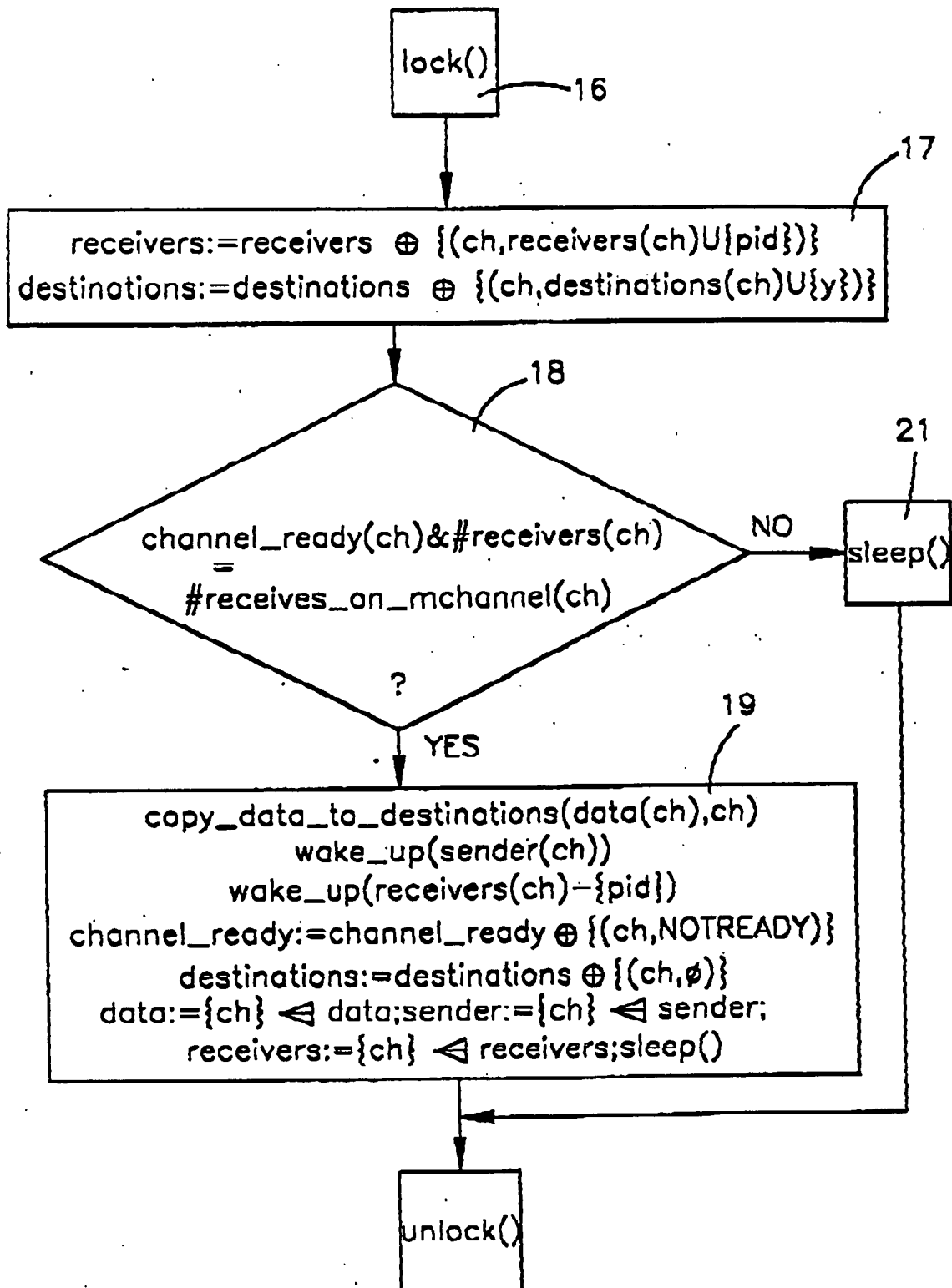


Algorithm for send

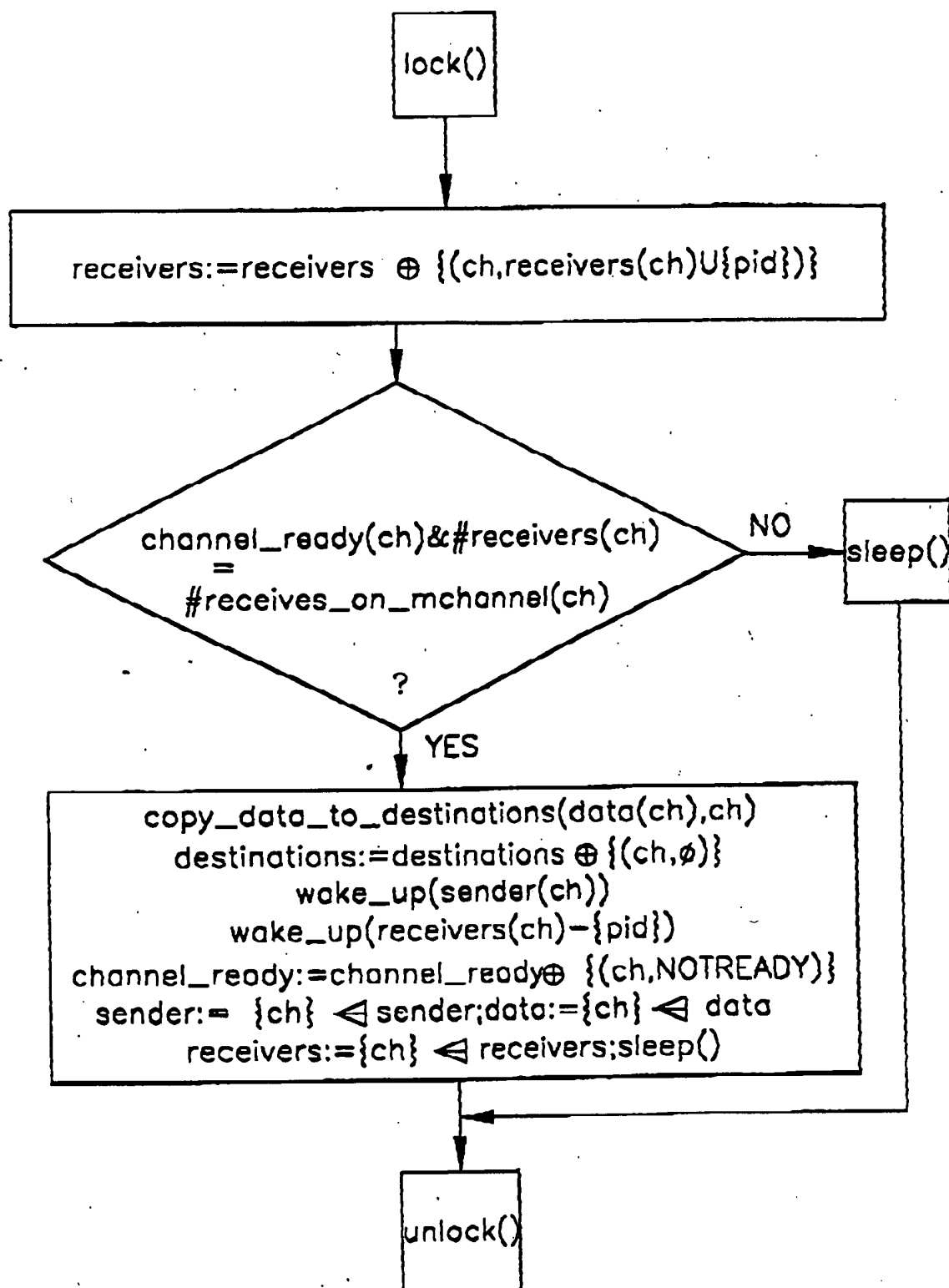
FIG 2

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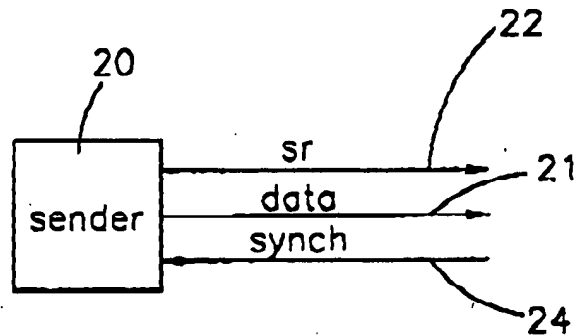
Algorithm for receive

FIG 3



Algorithm for the simplified receive

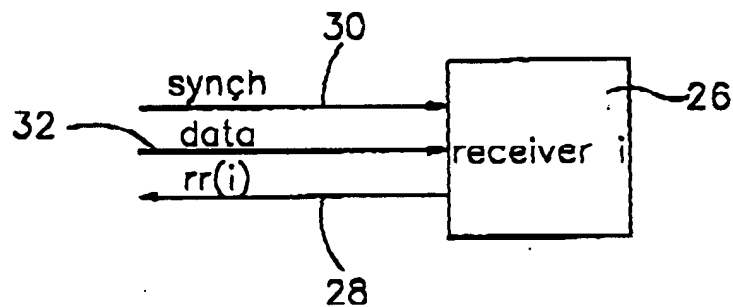
FIG 4



1. set 'data' to value to be sent
2. set 'sr' to true
3. wait until 'synch' is true
4. set 'sr' to false

Hardware implementation of send

FIG 5

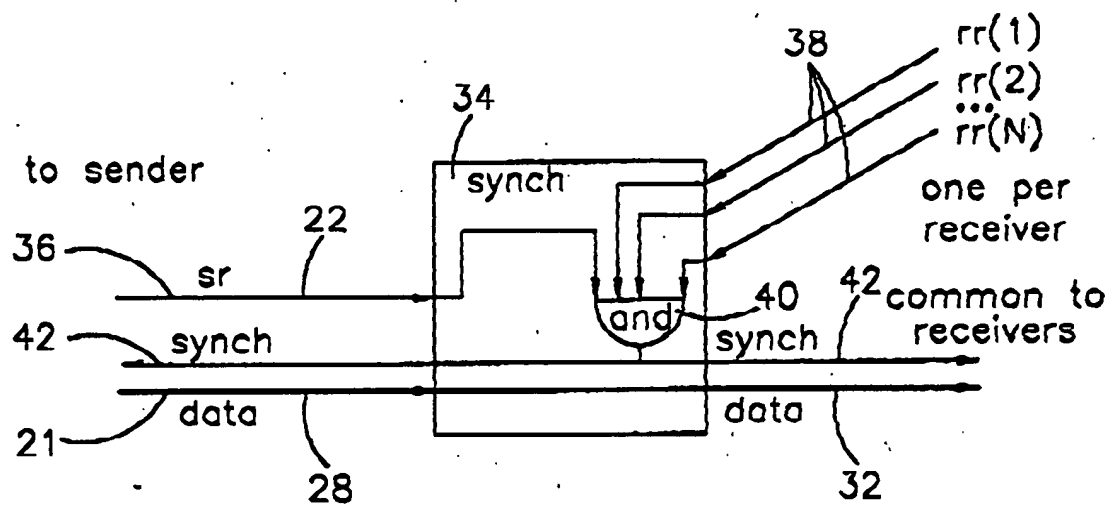


receive:

1. set 'rr(i)' to true
2. wait until 'synch' is true
3. copy data from 'data'
4. set 'rr(i)' to false

Hardware implementation of receive

FIG 6



Hardware implementation of synch

FIG 7